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at least two inductive elements (12;22;32) which are substantially concentric and/or substantially parallel to each other.

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12. (New) A circuit arrangement as claimed in claim 1, characterized in that the inductive elements (12;22;32) are magnetically fixedly coupled to each other.
13. (New) A circuit arrangement as claimed in claim 7, characterized in that each of the inductive elements (12;22;32) comprises one turn.
14. (New) A circuit arrangement as claimed in claim 7, characterized in that each of the inductive elements (12;22;32) comprises a plurality of turns.

#### **REMARKS**

This is responsive to the Office Action dated September 4, 2002 in which the Examiner rejects all the claims 1-11 as being anticipated by Petrovic (U.S. Patent No. 6,215,374) under 35 U.S.C. §102(e) and/or being obvious over Mizoguchi (U.S. Patent No. 5,583,474) in view of Petrovic under 35 U.S.C. §103(a). The Examiner further rejects claims 1, 3, 5 and 7 for being indefinite under 35 U.S.C. §112 because of improper claim language. Furthermore, the Examiner objects the Specification for lacking appropriate headings.

The applicant has added appropriate headings in the Specification to overcome the objection to the Specification. The applicant has amended claims 1, 3, 5 and 7 to define the invention more clearly, and believes the rejection under 35 U.S.C. §112 has been now overcome. The applicant has further added new claims 12-14, and respectfully traverses the rejections of the Examiner under 35 U.S.C. §102(e) and §103(a) based on the amended/added claims and the following detailed explanations.

The applicant does not agree with the assertion of the Examiner that claim 1 has been anticipated by Petrovic or is obvious over Mizohguchi in view of Petrovic. Petrovic discloses parallel and series tuned resonator circuit topologies in which microstrip transmission lines are utilized as effective inductor elements but not as resonators, thus achieving inexpensive, highly accurate and very small effective inductors. Mizohguchi discloses planar arrangement of coil conductors between insulation and magnetic layers for achieving great quality coefficient Q and large gain. However, neither of the cited patents has disclosed or implied that “all the resonant circuits of the circuit arrangement are arranged on only one metallization plate of the integrated circuit, having an essentially constant ohmic resistance”, which is a distinguishing feature of the present invention as defined in claim 1. As explained clearly in the Specification, with all resonant circuits being arranged on only one metallization plate having an essentially constant ohmic resistance, the circuit arrangement can always be implemented in the lowest ohmic metallization plate of the integrated circuit so that the resonant circuit losses are clearly reduced and the quality factor Q is significantly increased. Furthermore, the circuit arrangement can be realized in an inherently symmetrical arrangement and consequently combined with a differential and balanced circuit technique as preferred in the integrated technique (see, e.g., page 4, line 29 – page 5, line 11).

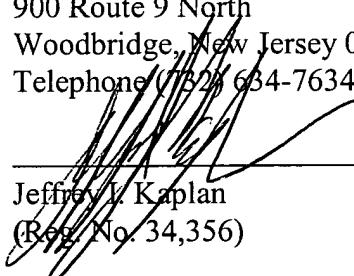
Therefore, the applicant believes that, with the above emphasized patentably distinguishing feature, claim 1 is patentable over the cited patents and their combination. At least for the same reason, claims 2-14, each of which includes all the limitations of claim 1, are also patentable.

The applicant therefore respectfully requests reconsideration and allowance in view of the above remarks and amendments. The Examiner is authorized to deduct additional fees believed due from our Deposit Account No. 11-0223.

Respectfully submitted,

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Dated: December 3, 2002

  
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Jeffrey I. Kaplan  
(Reg. No. 34,356)

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal service as first class mail, in a postage prepaid envelope, addressed to Box Non-Fee Amendment, Commissioner for Patents, Washington, D.C. 20231 on December 3, 2002.

Dated December 3, 2002 Signed  Print Name Paula Halsey

**MARKED-UP VERSION OF THE AMENDED CLAIMS 1, 3, 5 AND 7**

1. (Amended) A circuit arrangement for filtering and/or selecting single frequencies or frequency ranges [, particularly] of signals [intended for at least an integrated circuit and/or signals generated by at least an integrated circuit], said circuit arrangement (100) comprising at least two electric resonant circuits (10;20;30)
  - with at least an inductive element (12;22;32) and
  - at least a capacitive element (14;24;34),characterized in that the resonant circuits (10;20;30)[, particularly the inductive elements (12;22;32)] are magnetically fixedly coupled to each other, and in that [at least a part, preferably] all the resonant circuits (10;20;30) of the circuit arrangement (100) are arranged [at or on the integrated circuit, particularly] on only one metallization plate (40) of [the] an integrated circuit, having an essentially constant ohmic resistance.
3. (Twice Amended) A circuit arrangement as claimed in claim 1, characterized in that the individual resonant circuits (10;20;30) are essentially arranged in a planar way [on an outer side, particularly] on an outer surface area of the integrated circuit.
5. (Twice Amended) A circuit arrangement as claimed in claim 4, characterized in that the geometric structure is a circle, an oval, an ellipse, a square[,] or a rectangle [or the like].
7. (Twice Amended) A circuit arrangement as claimed in claim 1, characterized in that at least two inductive elements (12;22;32) [each comprise one, preferably a plurality of turns] which are substantially concentric and/or substantially parallel to each other.